A Study on Component Obsolescence Mitigation Strategies and Their Impact on R&M

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Key Words: Technology Obsolescence, Life Cycle, Cost of Ownership, Component Obsolescence, Logistic Downtime, Aircraft, Aging Aircraft, DMSMS

SUMMARY & CONCLUSIONS

This paper discusses the insights obtained from managing obsolescence mitigation strategies within the Department of Defense (DoD) during the last two decades. The numerous solutions investigated, from alternative or substitute components to complete system redesign can affect reliability and maintainability (R&M) as well as cost. However, with careful implementation of design practices many of the effects can be mitigated. In addition to presenting the common practices that mitigate the effect and risk of DMSMS, this paper also addresses the nonrecurring engineering (NRE) costs impacts associated with ensuring that solutions meet the original product’s system performance and R&M requirements.

1. INTRODUCTION

Rapid changes in item or material technology, uneconomical production requirements, and extended product life cycles are major causes of component obsolescence. Component obsolescence has affected the Department of Defense (DoD), the airline community, and many commercial industries. The DoD term for component obsolescence is Diminishing Manufacturing Sources and Material Shortages (DMSMS). This paper will use the term DMSMS. DMSMS can affect any phase of the product life cycle, from development to post production. DMSMS affects both cost and operations. If left unchecked DMSMS could cause increased downtime due to part shortages and decreased reliability if alternative solutions are not thoroughly qualified.

According to data collected by the Government Industry Data Exchange Program, 84% of discontinuance notices received by them are for electronic components, specifically active devices. The remaining 16% are for mechanical and passive devices. In addition to components, materials, chemical processes, and software could also become obsolete. The primary focus of this paper is on component obsolescence of active devices.

There has been significant work completed in the area of component obsolescence and solutions, however there have been no papers presented at reliability and maintainability symposiums that discuss the potential reliability issues associated with obsolescence.

2. DMSMS BACKGROUND

The rapid change in electronics and microcircuit technology continues to advance in accordance with Moore’s Law, which states that the density of semiconductors (e.g., fabrication process minimum feature size measured in micrometers) doubles about every 18 months. The electronic and microcircuit industries are increasingly more sensitive to rapidly improving consumer products rather than DoD and other industries (such as the airlines) that have to retain their equipment for much longer than 18 months due to economical considerations. As a result, the DoD continues to see the availability of military grade microcircuits decreasing and the number of DMSMS situations increasing. To relate obsolescence to non-DoD products, consider the rapid technology advancements in consumer products such as personal computers, cellular phones, and personal data assistants (PDAs) over the last decade, and how often they need to be upgraded or replaced. Technology is also at the maturity point for these types of products where level of repair analysis models will likely indicate that the most cost effective solution is to discard (throw-away) and replace them when they fail—if they fail at all during their useful life. But what if a computer purchased 10 years ago had to operate for another 20 years because there was
insufficient funding to acquire, integrate (install), and fully qualify a replacement with current technology. This is often the case with many DoD, airline, and NASA systems that rely on mission essential equipment and products that if replaced, the replacement must undergo extensive qualification testing. The DoD expects to operate its current fleet of tanks for another 30 years, the KC-135 aircraft for at least another 35 years, and the B-52 aircraft, which has flown for over 50 years, is expected to be in operation for at least 40 more years! For the past 20 years, the electronic digital systems used in the DoD have traditionally been designed using 5-Volt (5-V) technology. With continual technology advances, digital designers can now obtain higher densities and faster devices using 0.35 μm (3.3-V) or lower 0.18 μm process technology (2.5-V). The benefits of the lower voltage include efficiencies in operating temperatures, speed, power consumption, size, and densities. These benefits are ideal to allow the high-volume, low-power consumer products to decrease in size, but according to experts, this trend will contribute to an increase in obsolete components for the DoD and airlines. The question that remains is how will a computer or other legacy electronic system be maintained if either a failed component is no longer manufactured or software is no longer supported? Microelectronic and semiconductor obsolescence have been identified within the DoD since 1978 (Ref. 1). One of the first plans for managing obsolescence was developed for the U.S. Navy P-3 aircraft in 1984 (Ref 2). With over two decades of coping with DMSMS problems, the DoD has developed many resolution options and mitigation techniques that minimize the recurrence of them in the future. While each of the potential resolutions have cost implications, many may also affect reliability, maintainability, and supportability (RM&S).

3. RESOLUTION TECHNIQUES

Each of the services within the DoD have published a resolution guide identifying not only suggested resolutions but also policy and procedures:
- Naval Sea Systems Command (NAVSEASYSCOM)—Case Resolution Procedures Guide (NAVSEASYSCOM undated)
- Army Materiel Command (AMC)—DMSMS Case Resolution Guide (AMC undated)

The DMSMS resolutions contained in these documents are well known and usually are applied to existing or newly arising problems. The guides also provide information about coordinating actions with activities such as the Defense Logistics Agency (DLA), Defense Supply Center Columbus (DSCC), and the Defense MicroElectronics Activity (DMEA). The most commonly implemented resolution techniques for electronic components are described in the following sections along with their most prevalent RM&S considerations.

3.1 Existing Stock

The use of obsolete, surplus components currently owned by the product manufacturer or currently in-stock at a warehousing activity such as DLA. RM&S considerations include:
- Monitoring the possibility of dormant failure rates
- Preventing improper storage and handling
- Preventing improper application due to lot differences or part suffix differences

3.2 Reclamation

The use of an item found in equipment beyond economical repair at depots or surplus items from the Defense Reutilization and Marketing Service (DRMS). Reclamation can occur at the system, module, circuit board, or component level. Because of potential reliability problems, DMEA does not recommend reclamation of components from circuit boards. Even though removal processes have improved recently, reclamation of components should only be implemented as a last resort or temporary measure until other resolutions can be determined. RM&S considerations include:
- Refurbishment of circuit boards
- Conducting electrical and qualification testing
- Awareness of causing induced failures

3.3 Alternate

The use of part that is equal to or better than the part specified on a parts list. Such parts may be (1) listed in a specification or standard as superseding parts; (2) upgraded or better than original parts (such as JANTX in place of JAN, Standard Microcircuit Drawing parts in place of vendor unscreened parts, military temperature range parts in place of commercial temperature range parts); or (3) equivalent or interchangeable parts that are functionally the same, mechanically the same, and of the same quality as the specified parts, such as parts from a different vendor. RM&S considerations include:
- Conducting form, fit, function, interface (F3I) testing
- Providing the highest probability that reliability will not be reduced

3.4 Substitute

A part whose performance may be less capable than the part specified on a parts list for one or more reasons (e.g., quality or reliability level, tolerance, parametrics, temperature range). RM&S considerations include:
- Conducting F3I testing
- Performing qualification conformance inspection (QCI) testing
- Performing system level or in-circuit evaluations
- Minimizing the effect on reliability through careful selection, testing, and most importantly, understanding the physics of a failure of the design and installing it using best manufacturing practices. This assumes that
timing and other critical characteristics meet the design operating requirements.

3.5 Aftermarket

Aftermarket manufacturers such as Rochester Electronics or Lansdale Semiconductor are authorized by the original equipment manufacturer (OEM) to provide custom assembly of obsolete integrated circuits using existing wafer and die. In some cases, a manufacturer has acquired the photo mask sets or the entire assembly process from the OEM. A photo mask holds the pattern for each layer of an integrated circuit design. The manufacturer is then authorized to produce wafer, cut die, and package the cut die. In some cases, finished goods provided in a catalog may be considered as a substitute part. The RM&S considerations for aftermarket are the same as for substitute except that in-circuit evaluations are always needed due to variations in manufacturing processes.

3.6 Life of Type (LOT) and Bridge Buy

The purchase of a sufficient quantity of an obsolete item to meet the projected demands of the supported equipment for its expected operational lifetime. Unfortunately LOT buys seldom obtain the correct quantity of parts; either too many or too few components are procured, especially if the expected operational lifetimes are extended as has occurred with the B-52 aircraft. RM&S considerations for LOT buy is bridge buy, a purchase of limited-quantity components to support near-term requirements until a longer-term solution can be achieved.

3.7 Emulation

A manufacturing process that produces a substitute F31 item for the unobtainable item. Through microcircuit emulation, inventory reduction can be achieved because obsolete items can be replaced with devices that emulate the original and can be manufactured and supplied on demand. RM&S considerations for emulation are the same as for aftermarket.

3.8 Circuit Board Redesign

This paper classifies circuit board redesign into three categories. A Type 1 redesign typically involves adding jumper (white) wires or additional components such as resistors to adjust timing. A Type 2 redesign typically involves a board relayout to replace not more than one (1) or two (2) components. A Type 2 redesign is also known as a minor redesign. A Type 3 redesign often called a major redesign involves designing a DMSMS item out of the system and typically replaces the entire board with a F31 replacement. Type 3 redesigns are usually used as a last resort, but provide the opportunity to enhance system performance and improve R&M. Because substantial NRE and recurring logistics cost will accrue for Type 3 redesigns, they are most appropriate when a sufficient quantity of obsolete components are involved for the same circuit board (rules of thumbs have been discussed as greater than 5 to 10 components). The decision to implement a Type 3 redesign is made by conducting a cost trade-off analysis unique to each circuit board. An exception is when safety or performance measures are not meeting the requirements. RM&S considerations include:

- Awareness of induced failures possible for Type 1 and 2 only
- Awareness of an increased probability of failure for Type 1 and 2 only
- Revising and updating maintenance procedures
- Conducting mandatory qualification and system testing
- Anticipating software rehosting and testing

4. RESOLUTION COSTS

A decision support system often based on performance measures and total cost of ownership is typically used to select the best resolution(s) defined in Sections 3.1 – 3.8. For each resolution, recurring procurement costs and NRE costs are needed to determine which one is the most cost effective to implement. Recurring costs are specific to each system, component and resolution. The typical NRE costs associated with each resolution however have been collected (Ref. 3) and are presented in Table 4-1. These values are provided with the understanding that these can be used as estimated default values when actual documented data do not exist. If documented verifiable actual data are available, then the actual data should be used.

Table 4-1. Nonrecurring Engineering Cost Metrics (FY 2002)

<table>
<thead>
<tr>
<th>Resolution</th>
<th>90% Low (FY 02)</th>
<th>Average (FY 02)</th>
<th>90% High (FY 02)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Existing Stock</td>
<td>$0</td>
<td>$0</td>
<td>$0</td>
</tr>
<tr>
<td>Reclamation</td>
<td>1,000</td>
<td>2,000</td>
<td>3,000</td>
</tr>
<tr>
<td>Alternate</td>
<td>4,000</td>
<td>7,000</td>
<td>9,000</td>
</tr>
<tr>
<td>Substitute</td>
<td>15,000</td>
<td>19,000</td>
<td>24,000</td>
</tr>
<tr>
<td>Aftermarket</td>
<td>41,000</td>
<td>50,000</td>
<td>59,000</td>
</tr>
<tr>
<td>Emulation</td>
<td>55,000</td>
<td>72,000</td>
<td>89,000</td>
</tr>
<tr>
<td>Redesign—Minor</td>
<td>82,000</td>
<td>117,000</td>
<td>153,000</td>
</tr>
<tr>
<td>Redesign—Major</td>
<td>361,000</td>
<td>433,000</td>
<td>505,000</td>
</tr>
</tbody>
</table>

Table 4-1 provides the average values along with the lower and upper 90% s-confidence interval. Cost data were not collected for Type 1 redesign. In addition, cost metrics for LOT Buy are not included since they are based on the specific component failure rate, unit cost and system quantities. The basis for each of the cost values is provided in reference 4. Under certain circumstances, the resolutions identified may require any of the following additional actions: qualifying new sources, conducting radiation-hardening tests, conducting special tests for plastic-encapsulated microcircuits (PEMs), or program specific tests such as flight testing. The following should be considered:
New source qualification could increase cost; however, no standard value could be obtained because vendors typically amortize this as part of recurring cost.

If radiation-hardening testing is required, the cost metrics presented in Table 4-1 could increase an additional $5,000 (dose rate only) to $52,000 (dose rate, total dose, and single-event upset) and possibly as much as $82,000 for microprocessors.

If qualification testing for PEMs are required, each cost metric could increase from $600 (acoustic microscopy only) to $47,340 (full qualification of a 100-piece lot).

Program-specific test costs (e.g., flight test, aircraft ground test) are not included because standard values could not be obtained.

The NAVAIR Aging Aircraft Integrated Product Team (AAIPT) is developing cost models that incorporate program-specific test costs. The model includes hardware and software laboratory tests, ground tests, and flight tests. When these additional test costs are included, additional refinement is provided to the decision-maker. When these costs are included in the estimate, it is not unusual for Type 3 redesigns to reach $2M or more.

Adding to this cost impact is the frequency at which components are becoming obsolete. Ref. 5 indicates that, every year, 20% of the digital integrated circuits and 10% of the analog integrated circuits available to the DoD are going obsolete. Mitigating the cost impact and recurrence of obsolescence and planning ahead such that the lower cost resolutions can be implemented, can reduce the total cost of ownership for a system. Common practices that mitigate the impact and design strategies that reduce the future recurrence are available for incorporation into a DMSMS Management Plan.

5. COMMON PRACTICES

Minimizing the impact of component (parts) obsolescence and technical obsolescence risk is the heart of the DMSMS concern. Risk management techniques have been addressed by AFMC (Ref. 6), DMEA (Ref. 7), and the Electronics Industries Alliance (Ref. 8).

5.1 AFMC Case Resolution Guide

Some of the management and design approaches from Ref. 6 that relate to RM&S considerations are:

- Incorporate availability guarantees in contracts
- Create early-warning databases that contain complete indentured configuration data
- Implement open systems architecture (OSA) interface standards
- Design for obsolescence using very high speed integrated circuit (VHSIC) hardware description language (VHDL) to define component or system parameters
- Plan for periodic replacement (i.e., technology insertion or technology refresh)
- Select parts that are relatively new into their life cycles

5.2 DMEA Program Managers Handbook

Reference 7 provides three intensity levels of common practices that include activities that could be implemented to mitigate the risks of DMSMS:

- Level 1—Practices are implemented to resolve current obsolete items. Some of these activities may be considered reactive.
- Level 2—Minimal required practices are needed to mitigate the risk of future obsolete items. The majority of these activities are perceived as proactive.
- Level 3—Advanced practices are required to mitigate the risk of obsolescence when there is a high opportunity to enhance supportability or reduce total ownership cost (TOC). These activities are proactive and may require additional program funding.

Selecting a practice is influenced by the resources available to manage DMSMS. The practices associated with these levels form the basis of a program that can be implemented to mitigate the impact of DMSMS. Although an expense is associated with the implementation of a DMSMS program, cost avoidance and cost of ownership reductions can be realized from such a program. A list of the practices for each level is presented in Table 5-1. An event usually occurs that convinces the program manager that one or more practices need to be implemented. These events are called triggers. An example of a trigger would be higher management awareness of increased supportability problems.

<table>
<thead>
<tr>
<th>Level 1</th>
<th>Level 2</th>
<th>Level 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>DMSMS Focal Point</td>
<td>Awareness Training</td>
<td>Circuit Design</td>
</tr>
<tr>
<td>Awareness Briefing</td>
<td>DMSMS Prediction</td>
<td>VHDL</td>
</tr>
<tr>
<td>Internal Communications</td>
<td>DMSMS Steering Group</td>
<td>Technology Assessment</td>
</tr>
<tr>
<td>External Communications</td>
<td>COTS List</td>
<td>Electronic Data</td>
</tr>
<tr>
<td>DMSMS Plan</td>
<td>DMSMS Solution Database</td>
<td>Interchange (EDI)</td>
</tr>
<tr>
<td>Parts List Screening</td>
<td>Opportunity Index</td>
<td>Technology Insertion</td>
</tr>
<tr>
<td>Parts List Monitoring</td>
<td>Web Site</td>
<td></td>
</tr>
<tr>
<td>Resolution of Current Items</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Supportability Checklist</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Business case analyses have shown that the implementation of these practices can result in lowering the cost of resolving obsolescence problems and reducing TOC. It is important to note that as more practices are selected, the potential for reduction of TOC increases. The relative implementation cost versus potential for TOC reduction, along with a summary of the possible triggers, is shown in Figure 5-1.

5.2 EIA Bulletin GEB1 (EIA 2000)

Reference 8 describes methods that can be applied during system design to minimize the impact of future component obsolescence issues. These methods include:

- Technology Independence—use modular systems and VHDL modeling
- Software Portability—compile software independent of the target
- Technology Road Mapping—conduct market surveys of technology advances
- Technology Insertion—introduce new technology into a design
- Planned System Upgrade—bring the design up to date at defined intervals
- Life Cycle Analysis/DMSMS Monitoring—review current parts lists for discontinuance
- Part Selection Guidelines—select components early in their life cycle
- Part Description—implement a database to collect, store, and retrieve data

6. REPAIR CAPABILITY IMPACT ANALYSIS

Discontinuance notices originate when a part manufacturer announces that a part or a production line will be discontinued. Early notification of this event can help with the life-cycle planning needed to ensure that operational readiness levels can be maintained. Completing Level 1 practices such as part lists screening and monitoring and the Level 2 DMSMS prediction practice presented in Section 5 is just the first basic step in obsolescence management. Analysis of the current or potential problem parts identified by this first step is the key to cost effectively managing and resolving obsolescence problems.

As described in Section 4, NRE costs associated with each resolution could be significant. Planning ahead will not only help reduce the costs but also reduce operational impacts.

An example of how obsolescence can impact a products repair capability can now be investigated. Figure 6-1, ARINC’s Repair Capability Impact Analysis (RCIA) will be used for this investigation. Notional data are provided, however the overall analysis is based on current experiences in trying to keep aging aircraft flying. Let’s begin by determining the baseline repair capability. The baseline repair capability (BRC) quantifies how many expected failures there will be over the product’s useful life. The product could follow any reliability behavior such as a constant failure rate, wearout with age, or improvement with age. For this example we assumed the product, has a constant failure rate over its expected useful life, will no longer be produced once the production spares are purchased, and is a repairable system. Taking into account, expected failures, stock safety levels (to ensure adequate spares to fill the repair cycle pipeline), and planned attrition rates, a set number of spares is determined—creating the BRC.

The point where, due to attrition, the cumulative failure line crosses the BRC is known as the planned critical point. This point is the planned end of the product’s useful life. But, it may have to operate well beyond that time, as is the case for the B-52. As shown in Figure 6-1, failures after the planned critical point may not be supportable. With early planning and monitoring, steps such as incorporating a reliability improvement and upgrade program could extend the repair capability. Likewise, if the failures are monitored, unexpected wearout could also be identified early to avoid spare part shortages. DMSMS impacts are illustrated by the reduced repair capability (RRC) curve. The RRC curve illustrates the effect, over and above the normal attrition rate, that is caused when a product that fails cannot be repaired because the failed component is no longer manufactured and no resolutions are available!

When obsolescence management programs are implemented, planners could have procured sufficient quantities of alternate or substitute components or even initiated and possibly completed the engineering needed to incorporate a redesign. However, those resolutions can take from a few weeks, for alternate and substitute, to over a year for redesigns to be completed. Without early planning, maintenance and logistics downtime will increase as the product sits on the repair bench waiting for a part or part resolution. Left unchecked, operational readiness will be affected. When identifying solutions, take into account not only cost but also the RM&S considerations such as induced or dormant failures, revised maintenance procedures, and spares impact.

This is the just the beginning in documenting the effects obsolescence has on RM&S. Future work in this area will continue to develop additional resolutions, capture the RM&S considerations and costs associated with the resolutions, and most importantly identify new design strategies that will mitigate the impact of obsolescence. It is also expected that additional analyses and further refinement of models similar to the RCIA will continue.
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REFERENCES


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Walter Tomczykowski is the DMSMS Program Director at ARINC. He received an MS in Reliability Engineering from the University of Maryland. For the last 20 years he has been performing product support work in the areas of reliability, maintainability, and obsolescence management for a variety of Air Force, Navy and Army programs. Currently he provides DMSMS support to DMEA, the NAVAIR Aging Aircraft IPT, and the JTIDS program office. He is the author of the DMEA DMSMS Cost Factors, the DMSMS Program Managers Handbook, and the DMSMS Acquisition Guidelines. Previously his work in reliability has been published in the “Wiley Encyclopedia of Electrical and Electronics Engineering” and the “Product Reliability, Maintainability, and Supportability Handbook”.